```
ABORTS the current instruction and all instructions following in the pipe
IF Debug Exception THEN /* If a debug exception occurred /*
         IF Instruction_branch_delay_slot THEN
                  DEPC = PC - 4
                  Debug(DBD) = 1
         ELSE
                  DEPC = PC
                  Debug(DBD) = 0
         ENDIF
         CPU writes to Debug Register
                  - DINT, DIB, DDBS, DDBL, DBp or DSS to indicate the cause
                  - HALT to indicate that the internal system bus clock wa
                    stopped when the debug exception occurred
                  - DOZE to indicate that the processor was in some kind of low
                    power mode when a debug exception occurred
                  - DM to set Debug Mode
         IF EJTAGControlRegister(ProbTrap) = 1 THEN
                  PC = 0xFF20_0200; dmseg on the debug host
         ELSE
                  PC = 0xBFC0_0480; BEV space
         ENDIF
```

DEBUG EXCEPTION HANDLER

Use DeSave Register (CP0#31) to save one of the GPRs
Use the save GPR to save the rest of the context to a pre-determined memory area (e.g. EJTAG Probe)

Restore CPU State
Return to interrupted program
DERET

- resets the DM bit to 0
- sets PC to DEPC