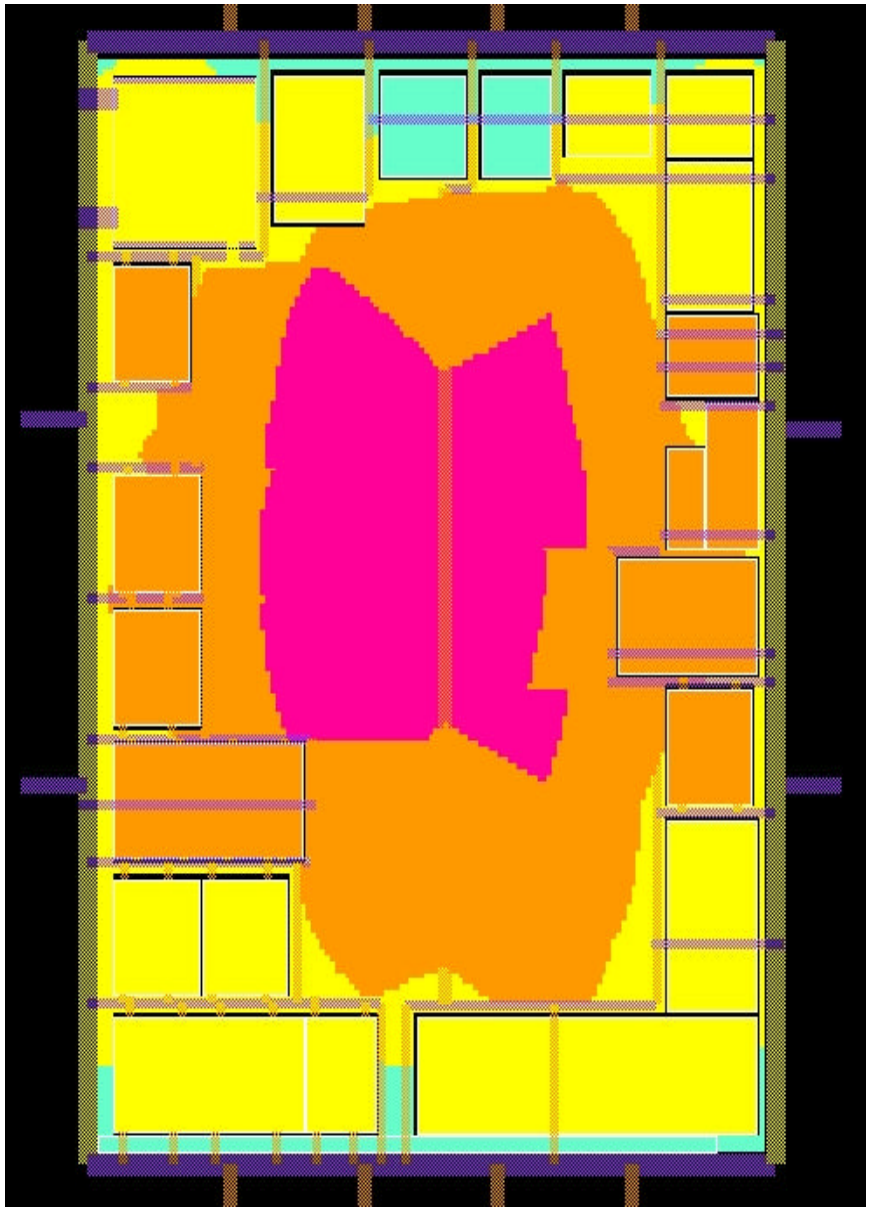
PowerPlanner™

Where Power Design Begins

Pre-RTL Power Mesh Design & Analysis

- **IR Drop Results consistent with final P&R analysis**
- **System-On-Chip Power Floorplanning**
- **Power Mesh, Rail & Via EM Analysis**
- **Reduce Costly Design Iterations**

"With PowerPlanner we were able to plan, design and perform reliability analysis on our power network before the RTL or gate level netlists were available. Being able to build a robust power network early in the design phase allowed us to significantly reduce the number of design iterations" said Thomas Snodgrass, Vice President of Engineering at ATI Technologies Inc.



RealPower™

A Simple Solution for a Complex Problem

Post Route Reliability Analysis

- Power Mesh extraction from DEF database
- IR Drop Results consistent with SPICE
- Power Mesh, Rail & Via EM Analysis
- Dynamic Power Calculation
- Optimize Power Mesh & Drive P&R tools

"Fujitsu has been actively working on the high performance DSM design issues. Our extensive chip design and internally developed tools along with IOTA's practical power design solution were key factors in our technical collaboration. By integrating IOTA's PowerPlanner and RealPower with Fujitsu's in-house power tools into IPSymphony™ we will provide best-in-class solutions to our customers,"
said David Dick, director of Advanced Technology Development Department (ATDD) for Worldwide System LSI Technologies (WWSLT), Fujitsu Limited.