

GLOBAL DESIGN TECHNOLOGY™

Simultaneous and Multidimensional Optimization

Hierarchical Design & Synthesis

Dolphin™ Global Design Technology

Place Route Timing Logic Clock Power

Straight to Silicon™

FULL-CHIP PHYSICAL DESIGN SOLUTION
BEST QUALITY RESULTS

GLOBAL OPTIMIZATION

Fluid Block™ Design

Fixed Partitions



Long inter-block wires
Cannot meet timing



GLOBALLY OPTIMIZED
INTERCONNECTS

MEET OR EXCEED TIMING

HIERARCHICAL DESIGN SUPPORT

FASTEST TIME-TO-SILICON

Hierarchical Design & Synthesis

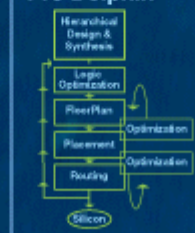
Dolphin™

Timing Logic Route Place

BUILT-IN DESIGN CONVERGENCE™
NO ITERATIONS

Multiple Iterations
Sequential Flow

Pre-Dolphin



TECHNOLOGY PLATFORM FOR THE FUTURE

Dolphin™ Complete Physical Design System

Dolphin Global Design Technology

Place Route Timing Logic Clock Power

32-bit OS
Uni or SMP

1 ... n

64-bit OS
Uni or SMP

1 ... n

HANDLE MULTIMILLION GATE
SOC DESIGNS

EXTENSIBLE SOFTWARE ARCHITECTURE
FOR ADDITIONAL DSM EFFECTS

UNMATCHED PERFORMANCE AND
INFINITELY SCALABLE

EXCEED SIA ROADMAP FOR
NEXT SEVERAL
PROCESS GENERATIONS